



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SATO

Attorney Docket No: 108397-00042

Serial Number: 09/843,820

Group Art Unit: 2816

Filed: April 30, 2001

Examiner: A. Luu

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CIRCUIT FOR
CHANGING TIMING OF INACTIVATING POWER-ON RESETTING CIRCUIT

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

December 3, 2002

Sir:

In reply to the Office Action dated September 5, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1-3 as follows:

1. (Three Times Amended) A semiconductor integrated circuit comprising:
a power-on resetting circuit for activating a reset signal in response to an initial supply of a power supply to initialize an internal circuit, and for inactivating the reset signal after a predetermined period following the initial supply to terminate an initialization of the internal circuit; and
a timing changing circuit for adjusting the predetermined period in accordance with an internal signal generated in the interior.

2. (Twice Amended) The semiconductor integrated circuit according to claim 1, wherein said timing changing circuit changes an inactivation timing of the reset signal in accordance with said internal signal to be set at a predetermined logic level

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John H

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